L Number	Hits	Search Text	DB	Time stamp
1	3	(716/\$).ccls. and (logic adj circuit) and (add\$ adj information)	USPAT;	2003/02/20 10:59
		and (passive adj component)	US-PGPUB;	i
	i		, EPO; JPO;	
	i		DERWENT;	
			IBM_TDB	
2	0	(logic adj circuit) and (addition adj information) and (passive	USPAT;	2003/02/20 11:00
		adj component)	US-PGPUB;	1
	İ		EPO; JPO;	
	1		DERWENT;	
	ļ		IBM_TDB	
	1 !	(logic\$ adj circuit) and (addition adj information) and (passive	USPAT:	1 2003/02/20 11:03
	i	adj component)	! US-PGPUB;	1
		•	EPO; JPO;	
			DERWENT;	
			[†] IBM_TDB	}
	1	(logic\$ adj circuit) and (deletion adj information) and (passive	USPAT;	+ 2003/02/20 11:0
	•	adj component)	US-PGPUB;	
		,,,	EPO; JPO;	
İ			DERWENT;	
[IBM_TDB	i
	28	(716/\$).ccls. and ((logical adj circuit) same (file or stor\$))	USPAT;	2003/02/06 16:5
1			US-PGPUB;	
			FPO; JPO;	1
			DERWENT,	•
			IBM_TDB	
	0	(wiring adj modification) and ((logical adj circuit) same (file or	USPAT;	2003/02/06 18:0
	J	stor\$))	US-PGPUB;	
		0.0.14//	EPO; JPO;	
			DERWENT;	
			IBM TDB	
	0	(716/\$).ccls. and (topology adj table) and (logical adj circuit)	USPAT:	2003/02/07 13:4
		(1, 10,4), oo lot and (topology bay take, and (12,3) and (12,3) and (1,5) an	US-PGPUB;	
			EPO; JPO;	t
			DERWENT;	1
	i		IBM TDB	1
	12	(716/\$).ccls. and (topology) and (logical adj circuit)	USPAT;	2003/02/07 14:3
		(1 (a.4).00.0. and (apology) and (agreement) an amy	US-PGPUB;	
•			EPO; JPO;	
			DERWENT;	
			IBM_TDB	ļ
_	129	(topology) and (logical adj circuit)	USPAT;	2003/02/07 14:3
	120	(topology) and (logical day on salt)	US-PGPUB;	
			EPO; JPO;	İ
			DERWENT;	!
1			' IBM_TDB	•
_	1	(topology) and (passive adj component) and (logical adj	USPAT;	1 2003/02/07 14:4
1	•	circuit)	US-PGPUB;	1
			EPO; JPO;	
			DERWENT:	I .
[IBM_TDB	1
_	13	(passive adj component) and (logical adj circuit)	USPAT;	2003/02/07 14:5
	13	passive day compensity and finglear day enoung	US-PGPUB;	1
!		1 1	EPO; JPO;	
ļ		!	DERWENT;	1
ĺ			IBM_TDB	1
	0	((delay adj line) adj (data or information)) and (logical adj	USPAT;	2003/02/07 15:0
-	U	circuit)	US-PGPUB;	
		on ours	EPO; JPO;	•
			DERWENT;	
1			IBM_TDB	1
_	219	(delay adj line) and (logical adj circuit)	USPAT;	2003/02/07 15:5
-	213	(delay daj line) and flegical daj elledity	US-PGPUB;	
i			EPO; JPO;	į
			DERWENT;	1

-	4	(part adj list) and (logical adj circuit)	USPAT;	2003/02/07 15:57
	ļ		US-PGPUB;	
	İ		EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	0	(logical adj circuit) and ((passive adj component) adj (delection	USPAT;	2003/02/07 17:11
		adj information))	US-PGPUB;	1
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	i
- ·	9	(logical adj circuit) and (resistance and capacitance) and	USPAT;	2003/02/07 17:13
	i 1	(peradj unit adj length)	US-PGPUB;	•
			EPO; JPO;	1
			DERWENT;	1
			IBM_TDB	1
-	12	(logical adj circuit) and (resistance and capacitance) and (per	USPAT;	2003/02/07 17:42
		adj unit adj length)	US-PGPUB;	
		,, 5,	EPO; JPO;	•
			, DERWENT;	
			IBM TDB	
_	1307	(table same (resistance and capacitance))	USPAT;	2003/02/07 17:44
		,,	US-PGPUB;	i
	1		EPO; JPO;	i .
			DERWENT;	
			IBM_TDB	
_	68	(716/\$).ccls. and (table same (resistance and capacitance))	USPAT;	2003/02/07 17:46
		(((((((((((((((((((((US-PGPUB;	!
			EPO; JPO;	,
·			DERWENT;	i ı
			IBM_TDB	!
_	57	(716/\$).ccls. and (rout\$ or wir\$) and (table same (resistance	USPAT;	2003/02/07 18:06
		and capacitance))	US-PGPUB;	
	1		EPO; JPO;	
			DERWENT;	
			IBM_TDB	,
_	7	(wire same (edit or modify)) and (logical adj circuit)	USPAT;	2003/02/14 15:14
		(Will ballio (ball of mount) // and (logical da) enouny	US-PGPUB:	1
			EPO; JPO;	•
			DERWENT;	1
			IBM TDB	!
_	0	((wire adj editing) adj unit) and (logic\$ adj circuit)	USPAT:	2003/02/14 15:46
		((,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	US-PGPUB;	
			EPO; JPO;	ļ
			DERWENT;	
			IBM_TDB	ı
-	1	(wire adj editing) and (logic\$ adj circuit)	USPAT;	2003/02/14 15:51
1		((())	US-PGPUB;	
			EPO; JPO;	!
			DERWENT:	1
	1		IBM_TDB	1
1_	10	((logic\$ adj circuit) adj modification)	USPAT;	2003/02/14 16:49
1	'	(124 an) and any and any	US-PGPUB;	
			EPO; JPO;	1
1			DERWENT;	4
1			IBM_TDB	
-	0	(wire adj editing) and (logical adj circuit)	USPAT;	2003/02/14 16:51
		\ \ \tag{\frac{1}{2}}	US-PGPUB;	1
İ			EPO; JPO;	1
	i		DERWENT;	
			IBM_TDB	1
-	0	(line adj editing) and (logical adj circuit)	USPAT;	1 2003/02/14 16:53
		, , , , , , , , , , , , , , , , , , , ,	US-PGPUB;	1
			EPO; JPO;	
1			DERWENT;	1
	1			

[-	8	(rerouting) and (logical adj circuit)	, USPAT;	2003/02/14 17:55 ,
		(, , , ,	US-PGPUB;	
1			EPO; JPO;	;
			DERWENT;	
			IBM_TDB	i
1_	0	(wire adj modification) and (logical adj circuit)	USPAT;	2003/02/14 17:56
	Ì		US-PGPUB;	
			EPO; JPO;	:
	ļ		, DERWENT;	•
1			IBM TDB	, ,

L Number	Hits	Search Text	DB	Time stamp
1	89	(logical adj circuit) and (inductor)	USPAT;	2003/02/20 13:34
			US-PGPUB;	
			EPO; JPO;	•
1			DERWENT;	<u> </u>
	ļ		IBM_TDB	
-	28	(716/\$).ccls. and ((logical adj circuit) same (file or stor\$))	USPAT;	2003/02/06 16:55
			US-PGPUB;	1
	1		EPO; JPO;	
į			DERWENT;	
			IBM_TDB	
-	0	(wiring adj modification) and ((logical adj circuit) same (file or	USPAT;	2003/02/06 18:02
		stor\$))	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	·
-	0	(716/\$).ccls. and (topology adj table) and (logical adj circuit)	USPAT;	2003/02/07 13:41
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	12	(716/\$).ccls. and (topology) and (logical adj circuit)	USPAT;	2003/02/07 14:32
	!		US-PGPUB;	1
İ	!		EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	129	(topology) and (logical adj circuit)	USPAT;	2003/02/07 14:38
			US-PGPUB;	
			EPO; JPO;	,
			DERWENT;	
			IBM_TDB	į į
-	1	(topology) and (passive adj component) and (logical adj	USPAT;	2003/02/07 14:45
		circuit)	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	13	(passive adj component) and (logical adj circuit)	USPAT;	2003/02/07 14:55
į			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	†
İ			IBM_TDB	i !
-	0	((delay adj line) adj (data or information)) and (logical adj	USPAT;	2003/02/07 15:08
		circuit)	US-PGPUB;	
			EPO; JPO;	
ł			DERWENT;	* '
l			IBM_TDB	
-	219	(delay adj line) and (logical adj circuit)	USPAT;	2003/02/07 15:52
			US-PGPUB;	!
			EPO; JPO;	
			DERWENT;	1
			IBM_TDB	;
-	4	(part adj list) and (logical adj circuit)	USPAT;	2003/02/07 15:57
			US-PGPUB;	'
			EPO; JPO;	!
			DERWENT;	
1			IBM_TDB	
-	0	(logical adj circuit) and ((passive adj component) adj (delection	USPAT;	2003/02/07 17:11
		adj information))	US-PGPUB;	1
		"	EPO; JPO;	
			DERWENT;	1
			IBM_TDB	
- !	9	(logical adj circuit) and (resistance and capacitance) and	USPAT;	2003/02/07 17:13
	•	(peradj unit adj length)	US-PGPUB;	!
,			EPO; JPO;	1
			DERWENT;	1
· i		· 1	IBM_TDB	,

-	12	(logical adj circuit) and (resistance and capacitance) and (per adj unit adj length)	USPAT; US-PGPUB;	2003/02/07 17:42
			EPO; JPO; DERWENT; IBM TDB	
-	1307	(table same (resistance and capacitance))	USPAT; US-PGPUB;	2003/02/07 17:44
			EPO; JPO; DERWENT; IBM_TDB	
-	68	(716/\$).ccls. and (table same (resistance and capacitance))	USPAT; US-PGPUB; EPO; JPO;	2003/02/07 17:46
	-	(740(0)) I and (and 0 and (abla case (societanes	DERWENT; IBM_TDB	2003/02/07 18:06
-	57	(716/\$).ccls. and (rout\$ or wir\$) and (table same (resistance and capacitance))	USPAT; US-PGPUB; EPO; JPO;	2003/02/07 18:00
-	7	(wire same (edit or modify)) and (logical adj circuit)	DERWENT; IBM_TDB USPAT;	2003/02/14 15:14
			US-PGPUB; EPO; JPO; DERWENT;	
-	0	((wire adj editing) adj unit) and (logic\$ adj circuit)	IBM_TDB USPAT; US-PGPUB; EPO; JPO;	2003/02/14 15:46
-	1	(wire adj editing) and (logic\$ adj circuit)	DERWENT; IBM_TDB USPAT;	2003/02/14 15:51
			US-PGPUB; EPO; JPO; DERWENT; IBM TDB	
-	10	((logic\$ adj circuit) adj modification)	USPAT; US-PGPUB; EPO; JPO;	2003/02/14 16:49
_	0	(wire adj editing) and (logical adj circuit)	DERWENT; IBM_TDB USPAT;	2003/02/14 16:51
		(wile day eating) and (logical day eneally	US-PGPUB; EPO; JPO; DERWENT;	
-	0	(line adj editing) and (logical adj circuit)	IBM_TDB USPAT; US-PGPUB;	2003/02/14 16:53
			EPO; JPO; DERWENT; IBM_TDB	
-	8	(rerouting) and (logical adj circuit)	USPAT; US-PGPUB; EPO; JPO;	2003/02/14 17:55
	0	(wire adj modification) and (logical adj circuit)	DERWENT; IBM_TDB USPAT;	2003/02/14 17:56
			US-PGPUB; EPO; JPO; DERWENT;	
-	3	(716/\$).ccls. and (logic adj circuit) and (add\$ adj information) and (passive adj component)	IBM_TDB USPAT; US-PGPUB;	2003/02/20 10:59
			EPO; JPO; DERWENT; IBM_TDB	<u> </u>

-	0	(logic adj circuit) and (addition adj information) and (passive	USPAT;	2003/02/20 11:00
		adj component)	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	•
-	1	(logic\$ adj circuit) and (addition adj information) and (passive	USPAT;	2003/02/20 11:02
		adj component)	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
ļ			IBM_TDB	0000/00/00 44.04
-	1	(logic\$ adj circuit) and (deletion adj information) and (passive	USPAT;	2003/02/20 11:04
1		adj component)	US-PGPUB;	
			EPO; JPO; DERWENT;	
ļ			IBM TDB	
	1	(logical adj circuit) and (part adj list)	USPAT;	2003/02/20 12:46
-	,	(logical adj circuit) and (part adj list)	US-PGPUB:	2000/02/20 12:10
			EPO; JPO;	
			DERWENT:	
			IBM TDB	
-	10	(logical adj circuit) and (component adj (list or table))	USPAT;	2003/02/20 13:30
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	